

L Number	Hits	Search Text	DB	Time stamp
1	1	5612577.pn.	USPAT; US-PGPUB DERWENT	2003/02/09 17:42 2003/02/09 17:44
2	1	de-19844333-\$.did.	DERWENT	2003/02/09 17:44
3	1	de-4429983-\$.did.	DERWENT	2003/02/09 17:44
4	1	de-4405566-\$.did.	DERWENT	2003/02/09 17:44
5	1	de-4238867-\$.did.	DERWENT	2003/02/09 17:45
6	1	de-3904771-\$.did.	DERWENT	2003/02/09 17:45
7	5	de-19844333-\$.did. de-4429983-\$.did. de-4405566-\$.did. de-4238867-\$.did. de-3904771-\$.did.	DERWENT	2003/02/09 17:54
8	1	1990-172420.NRAN.	DERWENT	2003/02/09 17:53
9	15	3904771.URPN.	USPAT	2003/02/09 17:53
10	1	5128500.pn.	USPAT; US-PGPUB	2003/02/09 17:55
11	2	5612577.pn. 5128500.pn.	USPAT; US-PGPUB	2003/02/09 18:08
12	4	("4473724" "5128500" "5442149" "5513077").PN.	USPAT	2003/02/09 17:55
13	1	5612577.URPN.	USPAT	2003/02/09 18:02
14	4	("2823272" "4473724" "4678872" "4695682").PN.	USPAT	2003/02/09 18:03
15	16	5128500.URPN.	USPAT	2003/02/09 18:05
16	2559	(307/9.1,10.1,147).ccls.	USPAT; US-PGPUB	2003/02/09 18:09
17	225	((307/9.1,10.1,147).ccls.) and (symmetric\$2 symmetry mirror mirrored)	USPAT; US-PGPUB	2003/02/09 18:38
18	57	((307/9.1,10.1,147).ccls.) and (symmetric\$2 symmetry mirror mirrored)) and ((circuit adj board\$1) (printed with board\$1) (printed adj circuit\$1) substrate\$1)	USPAT; US-PGPUB	2003/02/09 21:18
19	81	((307/9.1,10.1,147).ccls.) and (symmetric\$2 symmetry mirrored (mirror adj image))	USPAT; US-PGPUB	2003/02/09 18:49
20	54	((307/9.1,10.1,147).ccls.) and (symmetric\$2 symmetry mirrored (mirror adj image))) not (((307/9.1,10.1,147).ccls.) and (symmetric\$2 symmetry mirror mirrored)) and ((circuit adj board\$1) (printed with board\$1) (printed adj circuit\$1) substrate\$1))	USPAT; US-PGPUB	2003/02/09 18:45
21	185	((307/9.1,10.1,147).ccls.) and mirror\$1	USPAT; US-PGPUB	2003/02/09 18:46

22	0	(((307/9.1,10.1,147).ccls.) and (symmetric\$2 symmetry mirror mirrored)) not (((307/9.1,10.1,147).ccls.) and (symmetric\$2 symmetry mirror mirrored)) and ((circuit adj board\$1) (printed with board\$1) (printed adj circuit\$1) substrate\$1)) (((307/9.1,10.1,147).ccls.) and (symmetric\$2 symmetry mirror mirrored (mirror adj image))) not (((307/9.1,10.1,147).ccls.) and (symmetric\$2 symmetry mirror mirrored)) and ((circuit adj board\$1) (printed with board\$1) (printed adj circuit\$1) substrate\$1))) (((307/9.1,10.1,147).ccls.) and mirror\$1))	USPAT; US-PGPUB	2003/02/09 18:46
23	171	(180/321,322,323).ccls.	USPAT; US-PGPUB	2003/02/09 18:47
24	3	((180/321,322,323).ccls.) and ((circuit adj board\$1) (printed with board\$1) (printed adj circuit\$1) substrate\$1)	USPAT; US-PGPUB	2003/02/09 21:36
25	1	((180/321,322,323).ccls.) and (pan pans)	USPAT; US-PGPUB	2003/02/09 18:48
26	3	(((180/321,322,323).ccls.) and ((circuit adj board\$1) (printed with board\$1) (printed adj circuit\$1) substrate\$1)) (((180/321,322,323).ccls.) and (pan pans))	USPAT; US-PGPUB	2003/02/09 18:48
27	1	(((180/321,322,323).ccls.) and ((circuit adj board\$1) (printed with board\$1) (printed adj circuit\$1) substrate\$1)) (((180/321,322,323).ccls.) and (pan pans))) and (symmetric\$2 symmetry mirrored (mirror adj image))	USPAT; US-PGPUB	2003/02/09 18:51
28	2489	(361/736,737,777,784,803).ccls.	USPAT; US-PGPUB	2003/02/09 18:50
29	182	((361/736,737,777,784,803).ccls.) and (symmetric\$2 symmetry mirrored (mirror adj image))	USPAT; US-PGPUB	2003/02/09 21:37
30	716	((307/9.1,10.1,147).ccls.) and (rotat\$3 axis)	USPAT; US-PGPUB	2003/02/09 21:18
31	130	(((307/9.1,10.1,147).ccls.) and (rotat\$3 axis)) and ((circuit adj board\$1) (printed with board\$1) (printed adj circuit\$1) substrate\$1)	USPAT; US-PGPUB	2003/02/09 21:19

32	107	(((307/9.1,10.1,147).ccls.) and (rotat\$3 axis)) and ((circuit adj board\$1) (printed with board\$1) (printed adj circuit\$1) substrate\$1)) not (((307/9.1,10.1,147).ccls.) and (symmetric\$2 symmetry mirror mirrored)) and ((circuit adj board\$1) (printed with board\$1) (printed adj circuit\$1) substrate\$1)) (((307/9.1,10.1,147).ccls.) and (symmetric\$2 symmetry mirror mirrored (mirror adj image))) not (((307/9.1,10.1,147).ccls.) and (symmetric\$2 symmetry mirror mirrored)) and ((circuit adj board\$1) (printed with board\$1) (printed adj circuit\$1) substrate\$1))) (((307/9.1,10.1,147).ccls.) and mirror\$1) (((361/736,737,777,784,803).ccls.) and (symmetric\$2 symmetry mirror mirrored (mirror adj image))))	USPAT; US-PGPUB	2003/02/09 21:32
33	960	(361/784,803).ccls.	USPAT; US-PGPUB	2003/02/09 21:31
34	224	((361/784,803).ccls.) and (rotat\$3 axis)	USPAT; US-PGPUB	2003/02/09 21:40
35	182	(((361/784,803).ccls.) and (rotat\$3 axis)) not (((307/9.1,10.1,147).ccls.) and (symmetric\$2 symmetry mirror mirrored)) and ((circuit adj board\$1) (printed with board\$1) (printed adj circuit\$1) substrate\$1)) (((307/9.1,10.1,147).ccls.) and (symmetric\$2 symmetry mirror mirrored (mirror adj image))) not (((307/9.1,10.1,147).ccls.) and (symmetric\$2 symmetry mirror mirrored)) and ((circuit adj board\$1) (printed with board\$1) (printed adj circuit\$1) substrate\$1))) (((307/9.1,10.1,147).ccls.) and mirror\$1) (((361/736,737,777,784,803).ccls.) and (symmetric\$2 symmetry mirror mirrored (mirror adj image))) (((307/9.1,10.1,147).ccls.) and (rotat\$3 axis)) and ((circuit adj board\$1) (printed with board\$1) (printed adj circuit\$1) substrate\$1)) not (((307/9.1,10.1,147).ccls.) and (symmetric\$2 symmetry mirror mirrored)) and ((circuit adj board\$1) (printed with board\$1) (printed adj circuit\$1) substrate\$1)) (((307/9.1,10.1,147).ccls.) and (symmetric\$2 symmetry mirror mirrored (mirror adj image))) not (((307/9.1,10.1,147).ccls.) and (symmetric\$2 symmetry mirror mirrored)) and ((circuit adj board\$1) (printed with board\$1) (printed adj circuit\$1) substrate\$1))) (((307/9.1,10.1,147).ccls.) and mirror\$1) (((361/736,737,777,784,803).ccls.) and (symmetric\$2 symmetry mirror mirrored (mirror adj image))))	USPAT; US-PGPUB	2003/02/09 21:40
36	1433	200/5R.ccls.	USPAT; US-PGPUB	2003/02/09 21:36

37	264	200/5R.ccls. and ((circuit adj board\$1) (printed with board\$1) (printed adj circuit\$1) substrate\$1)	USPAT; US-PGPUB	2003/02/09 21:37
38	26	(200/5R.ccls. and ((circuit adj board\$1) (printed with board\$1) (printed adj circuit\$1) substrate\$1)) and (symmetric\$2 symmetry mirrored (mirror adj image))	USPAT; US-PGPUB	2003/02/09 21:37
39	149	(200/5R.ccls. and ((circuit adj board\$1) (printed with board\$1) (printed adj circuit\$1) substrate\$1)) and (rotat\$3 axis)	USPAT; US-PGPUB	2003/02/09 21:40

40	144	<p>((200/5R.ccls. and ((circuit adj board\$1) (printed with board\$1) (printed adj circuit\$1) substrate\$1)) and (rotat\$3 axis)) not (((307/9.1,10.1,147).ccls.) and (symmetric\$2 symmetry mirror mirrored)) and ((circuit adj board\$1) (printed with board\$1) (printed adj circuit\$1) substrate\$1))</p> <p>((((307/9.1,10.1,147).ccls.) and (symmetric\$2 symmetry mirrored (mirror adj image))) not (((307/9.1,10.1,147).ccls.) and (symmetric\$2 symmetry mirror mirrored)) and ((circuit adj board\$1) (printed with board\$1) (printed adj circuit\$1) substrate\$1)))</p> <p>((307/9.1,10.1,147).ccls.) and mirror\$1</p> <p>((361/736,737,777,784,803).ccls.) and (symmetric\$2 symmetry mirrored (mirror adj image))) (((307/9.1,10.1,147).ccls.) and (rotat\$3 axis)) and ((circuit adj board\$1) (printed with board\$1) (printed adj circuit\$1) substrate\$1)) not (((307/9.1,10.1,147).ccls.) and (symmetric\$2 symmetry mirror mirrored)) and ((circuit adj board\$1) (printed with board\$1) (printed adj circuit\$1) substrate\$1)) (((307/9.1,10.1,147).ccls.) and (symmetric\$2 symmetry mirrored (mirror adj image))) not (((307/9.1,10.1,147).ccls.) and (symmetric\$2 symmetry mirror mirrored)) and ((circuit adj board\$1) (printed with board\$1) (printed adj circuit\$1) substrate\$1))) ((307/9.1,10.1,147).ccls.) and mirror\$1</p> <p>((361/736,737,777,784,803).ccls.) and (symmetric\$2 symmetry mirrored (mirror adj image)))) (((361/784,803).ccls.) and (rotat\$3 axis)) not (((307/9.1,10.1,147).ccls.) and (symmetric\$2 symmetry mirror mirrored)) and ((circuit adj board\$1) (printed with board\$1) (printed adj circuit\$1) substrate\$1)) (((307/9.1,10.1,147).ccls.) and (symmetric\$2 symmetry mirrored (mirror adj image))) not (((307/9.1,10.1,147).ccls.) and (symmetric\$2 symmetry mirror mirrored)) and ((circuit adj board\$1) (printed with board\$1) (printed adj circuit\$1) substrate\$1))) ((307/9.1,10.1,147).ccls.) and mirror\$1</p> <p>((361/736,737,777,784,803).ccls.) and (symmetric\$2 symmetry mirrored (mirror adj image))) (((307/9.1,10.1,147).ccls.) and (rotat\$3 axis)) and ((circuit adj board\$1) (printed with board\$1) (printed adj circuit\$1) substrate\$1)) not (((307/9.1,10.1,147).ccls.) and (symmetric\$2 symmetry mirror mirrored)) and ((circuit adj board\$1) (printed with board\$1) (printed adj circuit\$1) substrate\$1)) (((307/9.1,10.1,147).ccls.) and (symmetric\$2 symmetry mirrored (mirror adj image))) not (((307/9.1,10.1,147).ccls.) and (symmetric\$2 symmetry mirror mirrored)) and ((circuit adj board\$1) (printed with board\$1) (printed adj circuit\$1) substrate\$1)))</p>	USPAT; US-PGPUB	2003/02/09 21:41
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